



Product Change Notification

PCN Number: WC143502

Notification Date: September 11, 2014

Title: Change in Package Marking for Selected AVR XMEGA D4/D3/A4U Microcontrollers

Product Identification:

Part numbers XMEGA D4	Part numbers XMEGA D3	Part numbers XMEGA A4U
ATxmega16D4-AU	ATxmega64D3-AU	ATxmega64A4U-AU
ATxmega16D4-AUR	ATxmega64D3-AUR	ATxmega64A4U-AUR
ATxmega16D4-MH	ATxmega64D3-MH	ATxmega64A4U-MH
ATxmega16D4-MHR	ATxmega64D3-MHR	ATxmega64A4U-MHR
ATxmega16D4-CU	ATxmega128D3-AU	ATxmega64A4U-CU
ATxmega16D4-CUR	ATxmega128D3-AUR	ATxmega64A4U-CUR
ATxmega32D4-AU	ATxmega128D3-MH	
ATxmega32D4-AUR	ATxmega128D3-MHR	
ATxmega32D4-MH	ATxmega192D3-AU	
ATxmega32D4-MHR	ATxmega192D3-AUR	
ATxmega32D4-CU	ATxmega192D3-MH	
ATxmega32D4-CUR	ATxmega192D3-MHR	
	ATxmega256D3-AU	
	ATxmega256D3-AUR	
	ATxmega256D3-MH	
	ATxmega256D3-MHR	

Please note: There will be products offered from Atmel and our sales channel with both current/old and new package markings for a time until transition to the latest die revision of the listed products is fully completed.

The new die revisions of the listed products were introduced in 2013 – please refer to the following PCNs for further details: WC131404, WC131403, WC131402A, WC131401, WC130901, WC124802, WC124804A, WC124803B.

Reason for Change:

- | | |
|--|---|
| ☐ Material / Composition | ☐ Manufacturing Location |
| ☒ Processing / Manufacturing | ☐ Quality / Reliability |
| ☐ Design / Firmware | ☐ Logistics |
| ☐ Datasheet | ☐ Other: |

Change Description:

Please refer to Appendix 1 for details on XMEGA D4 changes.
Please refer to Appendix 2 for details on XMEGA D3 changes.
Please refer to Appendix 3 for details on XMEGA A4U changes.

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Identification Method to Distinguish Change:			
Package markings are different than the other die revisions.			
Qualification Data:	☐ Available	☐ Will be available (mm/dd/yr):	☒ Not Applicable
Samples:	☐ Available	☐ Will be available (mm/dd/yr):	☒ Not Applicable
Quantifiable Impact on Quality & Reliability:			
None.			
Proposed First Ship Date*: Available now			
<i>*The Proposed First Ship Date is the forecasted date that a customer may expect to receive changed product. This is determined by the estimated date of inventory depletion on the PCN issue date. This may be affected by fluctuations in supply and demand. Consequently, although customers should be prepared to receive changed product on this date, Atmel will continue to ship pre-changed product until a time in which inventory has been depleted. This may result in pre-changed product being shipped to customers after this forecasted date.</i>			
Atmel Contact: Please contact your Atmel Sales Representative or Distributor for additional information (when replying via e-mail please include the PCN number in subject line).			
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CUSTOMER ACKNOWLEDGEMENT OF RECEIPT: Atmel requests you acknowledge receipt of this PCN. Please complete and email to pcnadm@atmel.com and the Atmel Contact listed above. In your acknowledgement, you can grant approval or request additional information. Atmel will deem this change accepted unless specific conditions of acceptance are provided in writing within 30 days from the date of this notice.			
To be completed by customer:			
☐ <u>Approved</u>			
☐ <u>Rejected (Please state reason for rejection):</u>			
Company: Name: Title: Date: Email Address: Location: Comments:			

APPENDIX 1: Change Description Details ATXMEGA D4

Old revision marking for ATxmega16D4-AU/AUR and ATxmega32D4-AU/AUR:	New revision marking for ATxmega16D4-AU/AUR and ATxmega32D4-AU/AUR:
Top Mark Atmel Logo CPN AU YYWW AU = Package type and plating material YYWW = Year/Week Bottom Assy Lot No. DDDDDR Country of Assy DDDDD = Die ID R = Revision letter	Top Mark Atmel Logo CPN -X YYWWR CC Assy Lot No. -X = Package plating material YYWW = Year/Week R = Revision letter CC = Country of Assy. Bottom BLANK

Table 1.

Old revision marking for ATxmega16D4-MH/MHR and ATxmega32D4-MH/MHR:	New revision marking for ATxmega16D4-MH/MHR and ATxmega32D4-MH/MHR:
Top Mark ATMEL CPN MHYYWW Country of Assy DDDDDR Assy Lot No. ATMEL = Company Name MH = Package type and plating material YYWW = Year/Week DDDDD = Die ID R = Revision letter Bottom BLANK	Top Mark Atmel Logo CPN -X YYWWR CC Assy Lot No. -X = Package plating material YYWW = Year/Week R = Revision letter CC = Country of Assy. Bottom BLANK

Table 2.

Old revision marking for ATxmega16D4-CU/CUR and ATxmega32D4-CU/CUR:	New revision marking for ATxmega16D4-CU/CUR and ATxmega32D4-CU/CUR:
Top Mark AT YYWWR CPN CU-K Assy Lot No. YYWW = Year/Week R = Revision letter CU = Package type and plating material K = Assembly location Code Bottom BLANK	Top Mark Atmel Logo CPN -X YYWWR CC Assy Lot No. -X = Package plating material YYWW = Year/Week R = Revision letter CC = Country of Assy. Bottom BLANK

Table 3.

APPENDIX 2: Change Description Details ATXMEGA D3

Old revision marking for ATxmega64D3-AU/AUR, ATxmega128D3-AU/AUR, ATxmega192D3-AU/AUR and ATxmega256D3-AU/AUR	New revision marking for ATxmega64D3-AU/AUR, ATxmega128D3-AU/AUR, ATxmega192D3-AU/AUR and ATxmega256D3-AU/AUR
Top Mark Atmel Logo CPN AU YYWW AU = Package type and plating material YYWW = Year/Week Bottom Assy Lot No. DDDDDR Country of Assy DDDDD = Die ID R = Revision letter	Top Mark Atmel Logo CPN -X YYWWR CC Assy Lot No. -X = Package plating material YYWW = Year/Week R = Revision letter CC = Country of Assy. Bottom BLANK

Table 4.

Old revision marking for ATxmega64D3-MH/MHR, ATxmega128D3-MH/MHR, ATxmega192D3-MH/MHR and ATxmega256D3-MH/MHR	New revision marking for ATxmega64D3-MH/MHR, ATxmega128D3-MH/MHR, ATxmega192D3-MH/MHR and ATxmega256D3-MH/MHR
Top Mark ATMEL CPN MHYYWW Country of Assy DDDDDR Assy Lot No. ATMEL = Company Name MH = Package type and plating material YYWW = Year/Week DDDDD = Die ID R = Revision letter Bottom BLANK	Top Mark Atmel Logo CPN -X YYWWR CC Assy Lot No. -X = Package plating material YYWW = Year/Week R = Revision letter CC = Country of Assy. Bottom BLANK

Table 5.

APPENDIX 3: Change Description Details ATXMEGA A4U

Old revision marking for XMEGA64A4U-AU/AUR	New revision marking for XMEGA64A4U-AU/AUR
Top Mark Atmel Logo CPN -X YYWWR CC Assy Lot No. -X = Package plating material YYWW = Year/Week R = Revision letter CC = Country of Assy. Bottom BLANK	Top Mark Atmel Logo CPN AU YYWW AU = Package type and plating material YYWW = Year/Week Bottom ##### DDDDDR N-A YYWW e3 ##### = Lot ID DDDDD = Die name R = Revision letter N = Fab location A = Assy location YYWW = Year/Week e3 = Material code

Table 6.

Old revision marking for XMEGA64A4U-MH/MHR	New revision marking for XMEGA64A4U-MH/MHR
Top Mark Atmel Logo CPN -X YYWWR CC Assy Lot No. -X = Package plating material YYWW = Year/Week R = Revision letter CC = Country of Assy. Bottom BLANK	Top Mark ATMEL CPN MHYYWW Country of Assy DDDDDR Assy Lot No. ATMEL = Company Name MH = Package type and plating material YYWW = Year/Week DDDDD = Die ID R = Revision letter Bottom BLANK

Table 7.

Old revision marking for XMEGA64A4U-CU/CUR	New revision marking for XMEGA64A4U-CU/CUR
Top Mark Atmel Logo XMEGA CPN-X YYWWR CC Assy Lot No. -X = Package plating material YYWW = Year/Week R = Revision letter CC = Country of Assy. Bottom BLANK	Top Mark AT YYWWR CPN CU-K Assy Lot No. YYWW = Year/Week R = Revision letter CU = Package type and plating material K = Assembly location Code Bottom BLANK

Table 8.