

PCN Number: RC140304
Notification Date*: February 19, 2014

Title: Migration to Marketing Revision Level B of the AT91SAM7X512

Product Identification:

Current Part Number	New Part Number
AT91SAM7X512-AU	AT91SAM7X512B-AU
AT91SAM7X512-AUR	AT91SAM7X512B-AUR
AT91SAM7X512-CU	AT91SAM7X512B-CU
AT91SAM7X512-CUR	AT91SAM7X512B-CUR

Reason for Change:

- | | |
|--|---|
| ☐ Material / Composition | ☐ Manufacturing Location |
| ☒ Processing / Manufacturing | ☐ Quality / Reliability |
| ☒ Design / Firmware | ☐ Logistics |
| ☒ Datasheet | ☐ Other: |

Change Description:

A complete list of differences between the marketing revision level (MRL) A and B can be found in chapter 41.2 of the AT91SAM7X series datasheet with Atmel document reference 6120K.

The ChipID has been incremented to:

AT91SAM7X512 Revision B chip ID is 0x275C 0A41

The Flash memory access time has been reduced as per the below table:

Flash Wait State	Read Operations	Maximum Operating Frequency (MHz)
0	1 cycle	16
1	2 cycles	32
2	3 cycles	48
3	4 cycles	55

Samples are available in Atmel Sample Center:

<https://samples.atmel.com/scripts/samplecenter.dll?atmel?cmd=menu>

Identification Method to Distinguish Change:

Marketing Revision Level (MRL) has been incremented on the top side marking to C.

Qualification Data:	☒ Available	☐ Will be available (mm/dd/yr):	☐ Not Applicable
Samples:	☒ Available	☐ Will be available (mm/dd/yr):	☐ Not Applicable

Quantifiable Impact on Quality & Reliability:

None

Forecasted Availability Date: February 20, 2014

Last Time Buy Date: May 20, 2014

Last Ship Date: February 20, 2015

**All orders placed after the notification date are non-cancellable and non-returnable (NCNR).*

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