



PCN / EOL Notification

Product Change Notification Number: RC103801

Notification Date*: September 21, 2010

Title: AT91SAM7S128 MRL B Revision and Ordering Code Change

Product Identification:

Current Part Number	New Part Number
AT91SAM7S128-AU-001	AT91SAM7S128C-AU
AT91SAM7S128-AU-999	AT91SAM7S128C-AU-999
AT91SAM7S128-MU	AT91SAM7S128C-MU
AT91SAM7S128-MU-999	AT91SAM7S128C-MU-999

Reason for Change:

☒ Design

☐ Manufacturing Location

☐ Processing

☐ Quality/Reliability

☐ Logistics

☐ Material

Change Description:

A complete list of differences between the marketing revision level (MRL) B and C can be found in chapter 40 of the datasheet of the AT91SAM7S-series with Atmel document reference 6175K.

In summary, multiple errata have been corrected on the ADC, PWM, SPI, SSC, TWI, USART and Voltage regulator. The flash access time increased which might require reprogramming of the number of wait states. The number of write/erase cycles of the Non Volatile Memory bits has been improved.

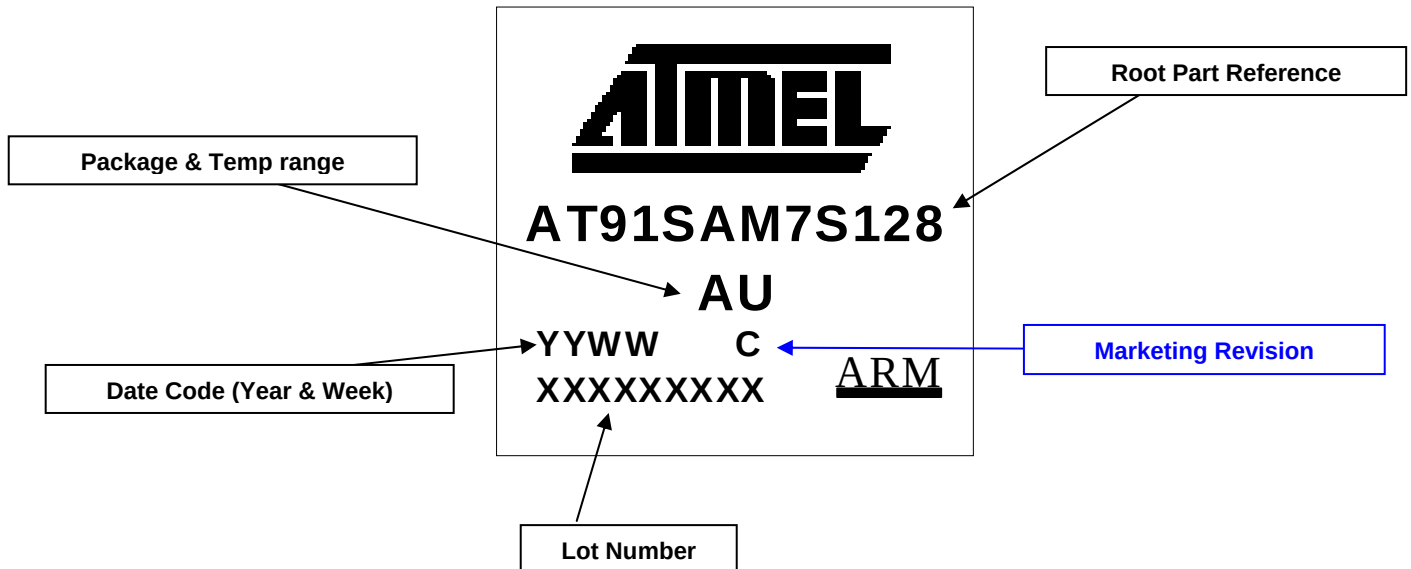
Identification Method to Distinguish Change:

Ordering code, as stated above in the product identification paragraph.

Marketing Revision on the topside marking will be incremented from B to **C**.

Chip-ID: AT91SAM7S128 Revision C chip ID is: 0x270A0742

Top side marking:



Qualification Data:	☒ available	☐ will be available in WW__	☐ not applicable
Samples:	☐ available	☒ will be available in Oct. 8, 2010	☐ not applicable

Quantifiable Impact on Quality & Reliability:

None

Forecasted Availability Date: January 5, 2011
Last Time Buy Date: March 15, 2011
Last Ship Date: September 15, 2011

* All orders placed after the notification date are **non-cancellable** and **non-returnable (NCNR)**.

Atmel Contact: pcnadm@atmel.com

Atmel will deem this change accepted unless specific conditions of acceptance are provided in writing within 30 days from the date of this notice. All correspondence must be sent to the Atmel Contact e-mail address listed above.

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