



## PCN / EOL Notification

**Product Change Notification Number: RC103203**

**Notification Date\*:** August 20, 2010

**Title:** AT91SAM7S512 MRL B Revision and Ordering Code Change, Adding UMC Manufacturing Location

### Product Identification:

| Current Part Number | New Part Number      |
|---------------------|----------------------|
| AT91SAM7S512-AU     | AT91SAM7S512B-AU     |
| AT91SAM7S512-AU-999 | AT91SAM7S512B-AU-999 |
| AT91SAM7S512-MU     | AT91SAM7S512B-MU     |
| AT91SAM7S512-MU-999 | AT91SAM7S512B-MU-999 |

The Ordering code, Marketing Revision Level and top side marking will change.

### Reason for Change:

☐ Design

☒ Manufacturing Location

☐ Processing

☐ Quality/Reliability

☐ Logistics

☐ Material

### Change Description:

Atmel introduces marketing revision level B on the AT91SAM7S512 to improve availability of product. This revision will be manufactured in both LFoundry, previously Atmel Rousset Fab7, and UMC (Taiwan) to ensure an uninterrupted flow of product to meet customer production demands. Both facilities will manufacture the parts to the same specification.

The AT91SAM7S512 MRL B is compatible in form, fit, and function to the MRL A material, which meet all datasheet specifications (6175J–ATARM–07-Dec-09) with the exception of the Flash memory access time. The Flash memory access time has been reduced as per the below table:

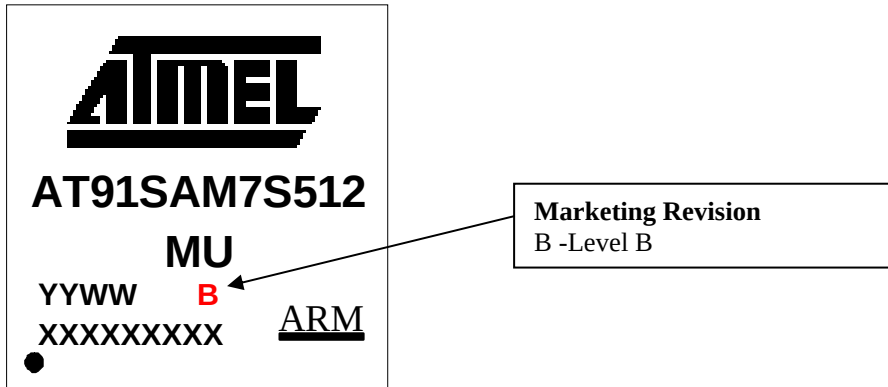
**Table 37-24.** Embedded Flash Wait States

The following maximum operating frequencies apply to the AT91SAM7S512 MRL B

| FWS <sup>(1)</sup> | Read Operations | Maximum Operating Frequency (MHz) |
|--------------------|-----------------|-----------------------------------|
| 0                  | 1 cycle         | 16                                |
| 1                  | 2 cycle         | 32                                |
| 2                  | 3 cycle         | 48                                |
| 3                  | 4 cycle         | 55                                |

Notes:

1. FWS = Flash Wait States

**Identification Method to Distinguish Change:**

YYWW = Data Code

XXXXXXXXXX = Lot Code

|                            |                                               |                                                    |                                         |
|----------------------------|-----------------------------------------------|----------------------------------------------------|-----------------------------------------|
| <b>Qualification Data:</b> | <input checked="" type="checkbox"/> available | <input type="checkbox"/> will be available in WW__ | <input type="checkbox"/> not applicable |
| <b>Samples:</b>            | <input checked="" type="checkbox"/> available | <input type="checkbox"/> will be available in WW__ | <input type="checkbox"/> not applicable |

**Quantifiable Impact on Quality & Reliability:**

None

**Forecasted Availability Date:** November 16, 2010 or as soon as customer approves after October 4th, 2010

**Last Time Buy Date:** June 15, 2011

**Last Ship Date:** December 15, 2011

\* All orders placed after the notification date are **non-cancellable** and **non-returnable (NCNR)**.

**Atmel Contact:** [pcnadm@atmel.com](mailto:pcnadm@atmel.com)

**Atmel will deem this change accepted unless specific conditions of acceptance are provided in writing within 30 days from the date of this notice. All correspondence must be sent to the Atmel Contact e-mail address listed above.**

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