



PCN / EOL Notification

Product Change Notification Number: RC084904

Date*: December 9, 2008

Title: AT91SAM7S64 Revision and Ordering Code Change

Product Identification:

Current Part Number	New Part Number
AT91SAM7S64-AU-001	AT91SAM7S64 B -AU
AT91SAM7S64-AU-800	AT91SAM7S64 B -AU
AT91SAM7S64-AU-999	AT91SAM7S64 B -AU-999
AT91SAM7S64-MU	AT91SAM7S64 B -MU
AT91SAM7S64-MU-999	AT91SAM7S64 B -MU-999

Reason for Change:

☒ Design

☐ Processing

☐ Logistics

☐ Manufacturing Location

☐ Quality/Reliability

☐ Material

Change Description:

The following errata have been corrected:

- PIO leakage on PA17-PA20

The errata is documented in the AT91SAM7S full datasheet dated 3 Dec 07, reference 6175H in section 40.3.2.1

A new errata has been identified on the SPI.

- In case the SPI is reset by software (SWRST in SPI control register) the command should be written twice.

Attached the updated errata sheet for the AT91SAM7S64B

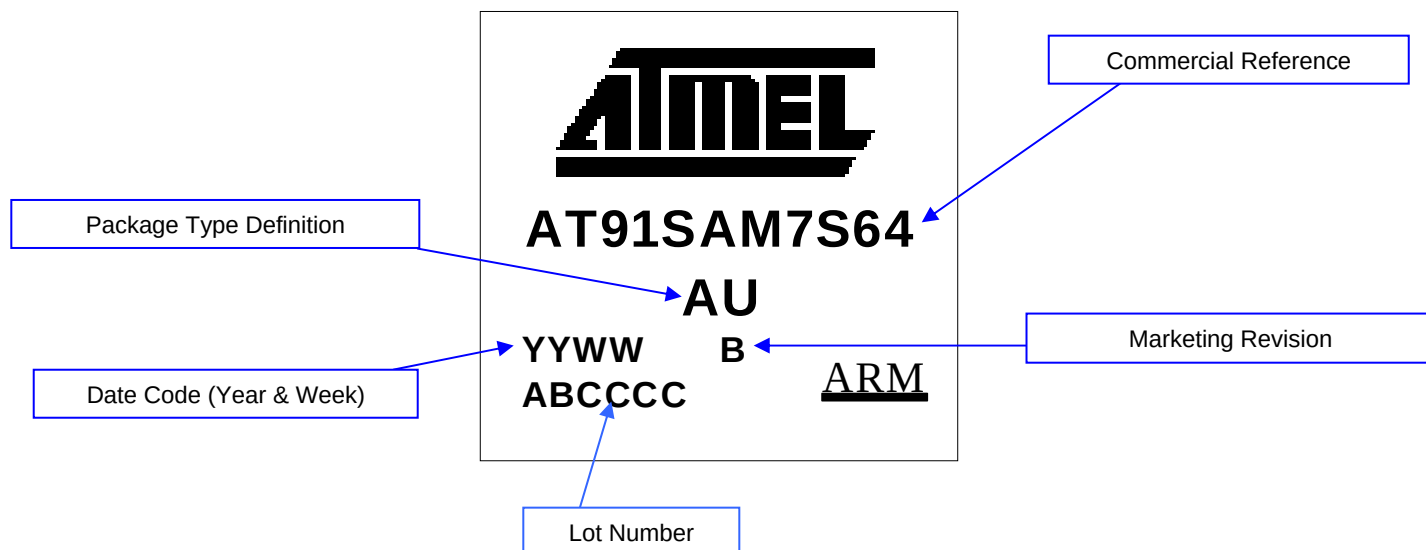
Identification Method to Distinguish Change:

Ordering code, as stated above in the product identification paragraph.

Marketing Revision on the topside marking will be incremented from **A** to **B**.

Chip-ID: AT91SAM7S64 Revision B chip ID is: 0x27090543

Top side marking:



Qualification Data:	☒ available	☐ will be available	☐ not applicable
Samples:	☐ available	☒ will be available 01/05/09	☐ not applicable

Quantifiable Impact on Quality & Reliability:

None

Estimated Availability Date*:	January 26, 2009
Last Time Buy Date:	June 12, 2009
Last Ship Date:	December 12, 2009

* All orders placed after the notification date are **non-cancellable** and **non-returnable (NCNR)**.

Atmel Contact: pcnadm@atmel.com

Atmel will deem this change accepted unless specific conditions of acceptance are provided in writing within 30 days from the date of this notice. All correspondence must be sent to the Quality Contact e-mail address listed above.

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