



Product Change Notification / SYST-23AJTS255

Date:

28-Dec-2020

Product Category:

32-bit Microcontrollers

PCN Type:

Document Change

Notification Subject:

Data Sheet - SAM E70/S70/V70/V71 Family Data Sheet

Affected CPNs:

[SYST-23AJTS255_Affected_CPN_12282020.pdf](#)

[SYST-23AJTS255_Affected_CPN_12282020.csv](#)

Notification Text:

SYST-23AJTS255

Microchip has released a new Product Documents for the SAM E70/S70/V70/V71 Family Data Sheet of devices. If you are using one of these devices please read the document located at [SAM E70/S70/V70/V71 Family Data Sheet](#).

Notification Status: Final

Description of Change: Updated the following sections:

Features - Updated 256 MB to 128 MB for the Memories section.

Configuration Summary - Removed USB type for LQFP Packages

Pinout - Corrected values in the Signal column for the 64-Lead Package Pinout

Power Considerations - Updated Power Supplies and added a Note to the end of the Backup Mode section

Product Mapping - Removed an erroneous figure reference.

Debug and Test Features - Added information regarding multidrop support to the second item in Embedded Characteristics

SAM-BA Boot Program - Updated the formatting of Embedded Characteristics to read properly and Updated the temperature verbiage in Hardware and Software Constraints

FFPI - Updated Device Configuration with new information for the Oscillator in Bypass mode and Updated Flash Write Command with additional text for the WP Command

EFC - Updated Embedded Flash Organization with new information for the Flash Address Space and Updated the following Sections with new information for Flash error:

Write Commands

Erase Commands

Lock Bit Protection

GPVNM Bit

User Signature Area

SUPC - Added a note to the Supply Monitor section

RSTC - Updated General Reset with a new signal for NRST on the General Reset Timing Diagram

PMC - Updated Main Crystal Oscillator in the Clock Generator section with new SLCK periods, Updated the Register Summary to properly display and Updated the bit numbering in the 31.20.23 PMC_PCER1 and 31.20.25 PMC_PCSR1 Registers

SDRAMC Updated the table for the CAS bit for the SDRAMC_CR Register

SMC - Updated the Register Offsets and Offset Equations for Proper display for the following registers: SMC_CYCLE

SMC_PULSE

SMC_MODE

SMC_SETUP

XDMAC - Updated DMA Controller Peripheral Connections with new information in the table for channel 1, transmit 31

GMAC - Updated MAC Transmit Block with the removal of erroneous text and Updated the register properties for the following registers:

OTLO

OTHI

FT

BCFT

MFT

ORLO

ORHI

FR

BCFR

MFR

USBHS - Updated the SPEED bit description in the USBHS_SR Register

HSMCI - Updated the following registers: HSMCI_RSPR: Added a new note and HSMCI_FIF0x: New register offset

QSPI - Updated the Description with new text for Master Mode and Updated the WDRBT Bit description in the QSPI_MR Register

TWIHS - Added a description to the LOCKCLR bit in the TWIHS_CR register

MCAN - Updated the reset property for the MCAN_CREL Register

AFEC - Added a new table to the TRANSFER bit of the AFEC_MR register

Electrical Characteristics for SAM V70/V71 - Updated the VDDIO Supply Monitor Table with a new note, Updated the table in 32.768 kHz Crystal Oscillator Characteristics and Updated the Max Spec in the table for XIN32 Clock Characteristics in Bypass Mode

Electrical Characteristics for SAM E70/S70 - Updated the VDDIO Supply Monitor Table with a new note, Updated the table in 32.768 kHz Crystal Oscillator Characteristics and Updated the Max Spec in the table for XIN32 Clock Characteristics in Bypass Mode

Impacts to Data Sheet: None

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

Date Document Changes Effective: 28 Dec 2020

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachments:

[**SAM E70/S70/V70/V71 Family Data Sheet**](#)

Please contact your local [**Microchip sales office**](#) with questions or concerns regarding this notification.

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