



Product Change Notification / SYST-18URGN802

Date:

12-Jan-2021

Product Category:

8-bit Microcontrollers

PCN Type:

Document Change

Notification Subject:

ERRATA - ATtiny3216/3217 Automotive Silicon Errata and Data Sheet Clarification Document Revision

Affected CPNs:

[SYST-18URGN802_Affected_CPN_01122021.pdf](#)

[SYST-18URGN802_Affected_CPN_01122021.csv](#)

Notification Text:

SYST-18URGN802

Microchip has released a new Product Documents for the ATtiny3216/3217 Automotive Silicon Errata and Data Sheet Clarification of devices. If you are using one of these devices please read the document located at [ATtiny3216/3217 Automotive Silicon Errata and Data Sheet Clarification](#).

Notification Status: Final

Description of Change:

1) Added errata

- a) CCL: 2.4.1 The CCL Must be Disabled to Change the Configuration of a Single LUT
- b) TCA: 2.5.1 Restart Will Reset Counter Direction in NORMAL and FRQ Mode
- c) TCB: 2.6.3 CCMP and CNT Registers Operate as 16-Bit Registers in 8-Bit PWM Mode
- d) TCD: 2.7.1 Asynchronous Input Events Not Working When TCD Counter Prescaler is Used

e) USART: 2.8.3 Start-of-Frame Detection Can Unintentionally be Enabled in Active Mode when RXCIF is '0'

2) Added data sheet clarifications

a) Package marking information

b) USART TX is double-buffered

Impacts to Data Sheet: None

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

Date Document Changes Effective: 12 Jan 2021

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachments:

[ATtiny3216/3217 Automotive Silicon Errata and Data Sheet Clarification](#)

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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