



Product Change Notification / SYST-15HJUP507

Date:

18-Dec-2020

Product Category:

32-bit Microcontrollers

PCN Type:

Document Change

Notification Subject:

ERRATA - SAM C20/C21 Family Silicon Errata

Affected CPNs:

[SYST-15HJUP507_Affected_CPN_12182020.pdf](#)

[SYST-15HJUP507_Affected_CPN_12182020.csv](#)

Notification Text:

SYST-15HJUP507

Microchip has released a new Product Documents for the SAM C20/C21 Family Silicon Errata of devices. If you are using one of these devices please read the document located at [SAM C20/C21 Family Silicon Errata](#).

Notification Status: Final

Description of Change:

Rev. G Document:

The following silicon issue was added in this revision:

- AC: 1.5.6 Spurious COMP Interrupt

The following silicon issue was updated with a new verbiage:

- EVSYS: 1.12.1 Generic Clock

Rev. F Document: (09/2020)

The following Data Sheet Clarifications were added in this revision, and the older Data Sheet Clarifications were removed.

- PTC Characteristics for SAM C20/21 E/G/J at 105°C
- PTC Characteristics for SAM C2
- Moisture Sensitivity Level

Impacts to Data Sheet: None

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

Estimated First Ship Date: 18 Dec 2020

NOTE: Please be advised that after the estimated first ship date customers may receive pre and post change parts.

Markings to Distinguish Revised from Unrevised Devices: Traceability Code

Attachments:

[SAM C20/C21 Family Silicon Errata](#)

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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