



Product Change Notification / SYST-15GCXE976

Date:

15-Jan-2021

Product Category:

Microprocessors

PCN Type:

Document Change

Notification Subject:

Data Sheet - SAMA5D3 Series Data Sheet Document Revision

Affected CPNs:

[SYST-15GCXE976_Affected_CPN_01152021.pdf](#)

[SYST-15GCXE976_Affected_CPN_01152021.csv](#)

Notification Text:

SYST-15GCXE976

Microchip has released a new Product Documents for the SAMA5D3 Series Data Sheet of devices. If you are using one of these devices please read the document located at [SAMA5D3 Series Data Sheet](#).

Notification Status: Final

Description of Change:

- 1) Section 7.1 Chip Identification: updated Chip ID.
- 2) Section 11.4.4.3 SD Card/eMMC Boot: added Note.
- 3) Section 42. Debug Unit (DBGU):
- 4) Section 42.6 Debug Unit (DBGU) User Interface: updated reset value for Chip ID Register.
- 5) Section 33. Image Sensor Interface (ISI): a) Added section Section 33.6 Register Write Protection. b) Updated all write-protected register descriptions.
- 6) Section 33.7.25 ISI Write Protection Status Register: updated bit WPVSRG description.
- 7) Section 54. Electrical Characteristics:
- 8) Section 54.14 SPI Timings: Updated formula in section Slave Write Mode.
- 9) Section 54.16 SSC Timings: Updated formula for SSC11.
- 10) Section 59. Errata: Added Section 59.4.1 SCKC: OSCSEL setting does not affect Slow Clock oscillator selection.

Impacts to Data Sheet: None

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

Date Document Changes Effective: 15 Jan 2021

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachments:

[SAMA5D3 Series Data Sheet](#)

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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