



Product Change Notification / SYST-10NIVQ377

Date:

17-Dec-2020

Product Category:

8-bit Microcontrollers

PCN Type:

Silicon Die Revision

Notification Subject:

ERRATA - ATtiny202/402 Silicon Errata and Data Sheet Clarification

Affected CPNs:

[SYST-10NIVQ377_Affected_CPN_12172020.pdf](#)

[SYST-10NIVQ377_Affected_CPN_12172020.csv](#)

Notification Text:

SYST-10NIVQ377

Microchip has released a new Product Documents for the ATtiny202/402 Silicon Errata and Data Sheet Clarification of devices. If you are using one of these devices please read the document located at [ATtiny202/402 Silicon Errata and Data Sheet Clarification](#).

Notification Status: Final

Description of Change: 1) Added silicon revision C 2) Added new errata: Device: Writing the OSCLOCK Fuse in FUSE.OSCCFG to '1' Prevents Automatic Loading of Calibration Values 3) ADC: Pending Event Stuck When Disabling the ADC 4) CCL: The CCL Must be Disabled to Change the Configuration of a Single LUT 5) TCA: Restart Will Reset Counter Direction in NORMAL and FRQ Mode 6) TCB: CCMP and CNT Registers Operate as 16-Bit Registers in 8-Bit PWM Mode 7) USART: Full Range Duty Cycle Not Supported When Validating LIN Sync Field , Open-Drain Mode Does Not Work When TXD is Configured as Output and Start-of-Frame Detection Can Unintentionally be Enabled in Active Mode when RXCIF is '0' 8) Added new data sheet clarifications: USART and Package Drawings

Impacts to Data Sheet: None

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

Estimated First Ship Date: 17 Dec 2020

NOTE: Please be advised that after the estimated first ship date customers may receive pre and post change parts.

Markings to Distinguish Revised from Unrevised Devices: Traceability Code

Attachments:

[ATtiny202/402 Silicon Errata and Data Sheet Clarification](#)

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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