



Product Change Notification / SYST-10KFAZ971

Date:

11-Dec-2020

Product Category:

16-Bit - Microcontrollers and Digital Signal Controllers

PCN Type:

Silicon Die Revision

Notification Subject:

ERRATA - dsPIC33CH128MP508 Family Silicon Errata and Datasheet Clarification

Affected CPNs:

[SYST-10KFAZ971_Affected_CPN_12112020.pdf](#)

[SYST-10KFAZ971_Affected_CPN_12112020.csv](#)

Notification Text:

SYST-10KFAZ971

Microchip has released a new Product Documents for the dsPIC33CH128MP508 Family Silicon Errata and Datasheet Clarification of devices. If you are using one of these devices please read the document located at [dsPIC33CH128MP508 Family Silicon Errata and Datasheet Clarification](#).

Notification Status: Final

Description of Change: 1) Added Silicon Revision B0. 2) Corrected the Features in Table 2 for Silicon Revision 10, 11 and 12. 3) Removed Work Around #2 from silicon issue 10 (UART). 4) Removed silicon issues 19 (UART) and 20 (UART) since they are no longer applicable. Please see datasheet clarification 5 (UART) for more information. 5) Changed the MPLAB® XC16 version to v1.61 or higher. 6) Added silicon issue 35 (UART) and 36 (Secondary CPU). 7) Added data sheet clarifications 5 (UART), 6 (SPI), 7 (Oscillator), 8 (Electrical Characteristics), 9 (ADC), 10 (DAC), 11 (PTG), 12 (PPS), 13 (Electrical Characteristics), 14 (MSTRPR Register) and 15 (MSTRPR Register).

Impacts to Data Sheet: None

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

Estimated First Ship Date: 15 Dec 2020

NOTE: Please be advised that after the estimated first ship date customers may receive pre and post change parts.

Markings to Distinguish Revised from Unrevised Devices: Traceability Code

Attachments:

[dsPIC33CH128MP508 Family Silicon Errata and Datasheet Clarification](#)

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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