



Product Change Notification / SYST-09TYIJ481

Date:

11-Feb-2021

Product Category:

8-bit Microcontrollers

PCN Type:

Document Change

Notification Subject:

ERRATA - ATmega808/809/1608/1609 Silicon Errata and Data Sheet Clarification

Affected CPNs:

[SYST-09TYIJ481_Affected_CPN_02112021.pdf](#)

[SYST-09TYIJ481_Affected_CPN_02112021.csv](#)

Notification Text:

SYST-09TYIJ481

Microchip has released a new Product Documents for the ATmega808/809/1608/1609 Silicon Errata and Data Sheet Clarification of devices. If you are using one of these devices please read the document located at [ATmega808/809/1608/1609 Silicon Errata and Data Sheet Clarification](#).

Notification Status: Final

Description of Change: 1) Device: 2.2.1 Writing the OSCLOCK Fuse in FUSE.OSCCFG to '1' Prevents Automatic Loading of Calibration Values

2) CCL: 2.5.1 The CCL Must be Disabled to Change the Configuration of a Single LUT

3) CRCSCAN: 2.6.1 The CRCSCAN Background Mode May Fail During an EEPROM or USERROW Write/Erase

4) TCA: 2.7.1 Restart Will Reset Counter Direction in NORMAL and FRQ Mode

5) TCB: 2.8.3 CCMP and CNT Registers Operate as 16-Bit Registers in 8-Bit PWM Mode

6) USART:

- 2.9.2 Open-Drain Mode Does Not Work When TXD is Configured as Output

- 2.9.3 Start-of-Frame Detection Can Unintentionally Be Triggered in Active Mode

Impacts to Data Sheet: None

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

Date Document Changes Effective: 11 Feb 2021

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachments:

[ATmega808/809/1608/1609 Silicon Errata and Data Sheet Clarification](#)

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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