



Product Change Notification / SYST-07HMVP661

Date:

11-Jan-2021

Product Category:

32-bit Microcontrollers

PCN Type:

Silicon Die Revision

Notification Subject:

ERRATA - SAM D5x/E5x Family Errata and Data Sheet Clarification

Affected CPNs:

[SYST-07HMVP661_Affected_CPN_01112021.pdf](#)

[SYST-07HMVP661_Affected_CPN_01112021.csv](#)

Notification Text:

SYST-07HMVP661

Microchip has released a new Product Documents for the SAM D5x/E5x Family Errata and Data Sheet Clarification of devices. If you are using one of these devices please read the document located at [SAM D5x/E5x Family Errata and Data Sheet Clarification](#).

Notification Status: Final

Description of Change:

The following errata had updates to their titles for clarification:

- 2.18.7 SERCOM-I2C: Repeated Start in High-Speed Master Write Operation
- 2.18.8 SERCOM-I2C: Repeated Start in High-Speed Master Read Operation
- 2.18.9 SERCOM-I2C: STATUS.CLKHOLD Bit in Master and Slave Modes
- 2.18.17 SERCOM-USART: Wakeup
- 2.18.18 SERCOM-USART: LENGTH
- 2.18.19 SERCOM-USART: Overconsumption in Standby mode

The following Errata were added in this revision:

- 2.2.3 AC: Standby Sleep Mode

- 2.2.4 AC: Debug Mode
- 2.4.7 CAN: Debug Message
- 2.4.8 CAN: Bus Errors
- 2.4.9 CAN: On Demand
- 2.4.10 CAN: Debug Mode
- 2.4.11 CAN: Debug Mode
- 2.5.2 CFD: Ready Bit Not Cleared
- 2.5.3 CFD: False Clock Failure Detection
- 2.5.4 CFD: False Clock Failure Detection
- 2.8.5 DFLL48M: DFLL48M Status Flags
- 2.8.6 DFLL48M: DFLL48M Status Flags
- 2.9.6 DAC: First Conversion
- 2.17.7 RTC: Tamper Detection Timestamp
- 2.17.8 RTC: General Purpose Registers
- 2.18.20 SERCOM: Wake-up Interrupt
- 2.18.21 SERCOM-SPI: Slave Data Preload
- 2.18.22 SERCOM-SPI: Hardware Slave Select Control
- 2.20.2 TC: Retrigger
- 2.21.5 TCC: Re-trigger in RAMP2 Operations
- 2.21.6 TCC: Re-Trigger
- 2.21.7 TCC: RAMP2 Operations
- 2.21.8 TCC: Dithering Mode
- 2.22.2 PDEC: Angular and Revolution Counters
- 2.24.1 EVSYS: Synchronous Mode
- 2.24.2 EVSYS: Software
- 2.25.1 TRNG: Over consumption

Impacts to Data Sheet: None

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

Date Document Changes Effective: 11 Jan 2021

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachments:

SAM D5x/E5x Family Errata and Data Sheet Clarification

Please contact your local **Microchip sales office** with questions or concerns regarding this notification.

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