



Product Change Notification / SYST-07ATNE052

Date:

08-Jan-2021

Product Category:

32-bit Microcontrollers

PCN Type:

Silicon Die Revision

Notification Subject:

ERRATA - SAM L10/L11 Family Silicon Errata and Data Sheet Clarification

Affected CPNs:

[SYST-07ATNE052_Affected_CPN_01082021.pdf](#)

[SYST-07ATNE052_Affected_CPN_01082021.csv](#)

Notification Text:

SYST-07ATNE052

Microchip has released a new Product Documents for the SAM L10/L11 Family Silicon Errata and Data Sheet Clarification of devices. If you are using one of these devices please read the document located at [SAM L10/L11 Family Silicon Errata and Data Sheet Clarification](#).

Notification Status: Final

Description of Change: This revision contains numerous typographical updates, and formatting updates. The following errata were updated with new verbiage:

- DMAC: 2.4.1 Concurrent Channels Trigger
- EIC: 2.5.1 PAC Protection
- OPAMP: 2.8.1 Reference Buffer
- SERCOM I2C: 2.10.8 Status Flags
- SERCOM I2C: 2.10.9 Status Flags
- SERCOM SPI: 2.11.1 Data Preload
- SERCOM USART: 2.12.5

The following errata were added in this revision:

- ADC: 2.1.2 Offset Correction
- Device: 2.3.2 Standby Entry

- RTC: 2.9.9 Tamper Detection
- RTC: 2.9.10 Tamper Detection
- RTC: 2.9.11 General Purpose Registers
- SERCOM I2C: 2.10.10 Status Flags
- SERCOM SPI: 2.11.2 Hardware Slave Select Control
- SERCOM SPI: 2.11.3 Slave Data Preload
- SERCOM SPI: 2.11.4 Wakeup Interrupt
- SERCOM USART: 2.12.6 Overconsumption in Standby Mode
- SERCOM USART: 2.12.7 Wakeup Interrupt
- TC: 2.13.3 Retrigger
- OSC32KCTRL: 2.16.2 1024 Hz Clock Output
- OSC32KCTRL: 2.16.3 Clock Failure Detection
- OSC32KCTRL: 2.16.4 XOSC32K Ready Bit
- EVSYS: 2.18.1 Synchronous and Resynchronized Modes
- EVSYS: 2.18.2 Synchronous Mode
- OSCCTRL: 2.19.1 Clock Failure Detection
- OSCCTRL: 2.19.2 Clock Failure Detection
- OSCCTRL: 2.19.3 XOSC Ready Bit
- PORT: 2.20.1 IOBUS
- TRAM: 2.21.1 PAC Protection

Impacts to Data Sheet: None

Reason for Change: To Improve Productivity

Change Implementation Status: Complete

Date Document Changes Effective: 08 Jan 2021

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachments:

[SAM L10/L11 Family Silicon Errata and Data Sheet Clarification](#)

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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