



## Product Change Notification / SYST-03QQZX502

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**Date:**

04-Feb-2021

**Product Category:**

Clock and Timing - Clock and Data Distribution

**PCN Type:**

Document Change

**Notification Subject:**

Data Sheet - ZL40268 - Low Skew, Low Additive Jitter 8 Output HCSL/LVDS/LVPECL Fanout Buffer with Per Enable Control

**Affected CPNs:**

[SYST-03QQZX502\\_Affected\\_CPN\\_02042021.pdf](#)

[SYST-03QQZX502\\_Affected\\_CPN\\_02042021.csv](#)

**Notification Text:**

SYST-03QQZX502

Microchip has released a new Product Documents for the ZL40268 - Low Skew, Low Additive Jitter 8 Output HCSL/LVDS/LVPECL Fanout Buffer with Per Enable Control of devices. If you are using one of these devices please read the document located at [ZL40268 - Low Skew, Low Additive Jitter 8 Output HCSL/LVDS/LVPECL Fanout Buffer with Per Enable Control](#)

**Notification Status:** Final

**Description of Change:**

1) Updated Pin Names for Pins 42 and 43.

**Impacts to Data Sheet:** None

**Reason for Change:** To Improve Manufacturability

**Change Implementation Status:** Complete

**Date Document Changes Effective:** 04 Feb 2021

**NOTE:** Please be advised that this is a change to the document only the product has not been changed.  
**Markings to Distinguish Revised from Unrevised Devices:** N/A

## Attachments:

ZL40268 - Low Skew, Low Additive Jitter 8 Output HCSL/LVDS/LVPECL Fanout Buffer with Per Enable Control

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

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