

PCN Issue date: 07.09.2022

PCN effective date: 30.11.2022

Products affected: INAP590TAQ

Change description:

Change from conventional load board to FPGA load board at final test.

Reason for change:

Improvement of the test coverage.

Some high speed tests can only be implemented in this configuration.

Impact of change:

No change in form, fit and function of the product.

Traceability:

With clean date code.

Additional Information:

Delta Qualification Plan according ZVEI Change ID:

- SEM-EQ-03
- Evaluation level: C (Component level)