



PCN / EOL Notification

Product Change Notification Number: **CC121306A (REVISED 02/19/13)** Notification Date: November 6, 2012
See Below Deleted Text

Title: 2-Kbit Two-wire Interface Industrial Temperature Grade (-40C to 85C) Serial EEPROM (AT34C02C) Process Geometry Shrink and Device Enhancement

Product Identification:

AT34C02C All Packages of the Industrial Temperature Grade (-40C to +85C)

Reason for Change:

☒ Material / Composition

☒ Processing / Manufacturing

☒ Design / Firmware

☐ Logistics

☐ Manufacturing Location

☐ Quality / Reliability

Change Description:

Atmel has performed a process geometry shrink of the Serial EEPROM Industrial Temperature Grade AT34C02C device (Two-wire interface, 2-Kbit density) from .35 μ to .25 μ . The catalog part number AT34C02C will be replaced by AT34C02D (see Table 1). The new devices are pin-to-pin and functionally backward compatible with the current AT34C02C devices with the following exception and enhancements.

Copper (Cu) bond wire usage

In order to increase manufacturing flexibility and to ensure a long-term continuity of supply, Atmel will manufacture (SOIC, TSSOP and UDFN) packages using ~~both gold (Au) and~~ copper bond (Cu) wires. In addition, Atmel will add ASE to its list of qualified UDFN assembly and test location to enhance capacity. ~~Atmel reserves the right to ship devices with either gold or copper bond wires.~~

Identification Method to Distinguish Change:

NEW catalog part numbers use "D" suffix for the device revision (AT34C02C changes to AT34C02D). Please refer to Attachment A for part marking scheme.

Table 1

This is the listing for standard datasheet offering, PCN also applies to all (customer specific) special CAN part numbers that are not listed in the table below:

EOL Part Number	Replace Part Number	Package	Carrier Type
AT34C02C-TH-B	AT34C02D-XHM-B	TSSOP	Bulk
AT34C02C-TH-T	AT34C02D-XHM-T	TSSOP	T/R, 5K per reel
AT34C02CN-SH-B	AT34C02D-SSH-M-B	JEDEC SOIC	Bulk
AT34C02CN-SH-T	AT34C02D-SSH-M-T	JEDEC SOIC	T/R, 4K per reel
AT34C02CU3-UU-T	AT34C02D-CUM-T	VFBGA	T/R, 5K per reel
AT34C02CY6-YH-T	AT34C02D-MAHM-T	UDFN	T/R, 5K per reel

Qualification Data:

☒ Available

☐ Will be available (mm/dd/yr):

☐ Not Applicable

Samples:	☒ Available	☒ Will be available (mm/dd/yr): Q4 2012 (Copper bond wired UDFN sample)	☐ Not Applicable
Quantifiable Impact on Quality & Reliability: None			
Forecasted Availability Date: Available now Last Time Buy Date: March 21, 2013 Last Ship Date: September 21, 2013 <i>*The Proposed First Ship Date is the forecasted date that a customer may expect to receive changed product. This is determined by the estimated date of inventory depletion on the PCN issue date. This may be affected by fluctuations in supply and demand. Consequently, although customers should be prepared to receive changed product on this date, Atmel will continue to ship pre-changed product until a time in which inventory has been depleted. This may result in pre-changed product being shipped to customers after this forecasted date.</i>			
Atmel Contact: Please contact your Atmel Sales Representative or Distributor for additional information (when replying via e-mail please include PCN number in subject line).			
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CUSTOMER ACKNOWLEDGEMENT OF RECEIPT: Atmel requests you acknowledge receipt of this PCN. Please complete and email to pcnadm@atmel.com and the Atmel Contact listed above. In your acknowledgement, you can grant approval or request additional information. **Atmel will deem this change accepted unless specific conditions of acceptance are provided in writing within 30 days from the date of this notice.**

Company: Name: Title: Date: Email Address: Location: Comments:	
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Attachment A

AT34C02D: Package Marking Information

8-lead SOIC	8-lead TSSOP
8-lead UDFN	8-ball VFBGA
2.0 x 3.0 mm Body 	1.5 x 2.0 mm Body

Note 1: @ designates pin 1

Note 2: Package drawings are not to scale

Catalog Number Truncation			
AT34C02D		Truncation Code ###: 34D	
Date Codes			Voltages
Y = Year	M = Month	WW = Work Week of Assembly	M: 1.7V min
2: 2012 6: 2016	A: January	02: Week 2	
3: 2013 7: 2017	B: February	04: Week 4	
4: 2014 8: 2018	...	...	
5: 2015 9: 2019	L: December	52: Week 52	
Country of Assembly		Lot Number	Grade/Lead Finish Material
@ = Country of Assembly		AAA...A = Atmel Wafer Lot Number	U: Industrial/Matte Tin H: Industrial/NiPdAu
Trace Code			Atmel Truncation
XX = Trace Code (Atmel Lot Numbers Correspond to Code) Example: AA, AB.... YZ, ZZ			AT: Atmel ATM: Atmel ATML: Atmel

3/5/12

Package Mark Contact: DL-CSO-Assy_eng@atmel.com	TITLE	DRAWING NO.	REV.
	34C02DSM, AT34C02D Package Marking Information	34C02DSM	C